



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re PATENT application of:)

Shunpei YAMAZAKI et al.)

Art Unit: 2822

Serial No. 08/691,434)

Examiner: M. Wilczewski

Filed: 08/02/1996)

For: METHOD OF FABRICATING)

SEMICONDUCTOR DEVICES AND)

APPARATUS FOR PROCESSING A)

SEMICONDUCTOR)

RECEIVED
SEP 24 2001
Technology Center 2100
#58
10-2901
Payton

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231, on 9/21/2001.

SUPPLEMENT TO PRIOR INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents

Washington, D.C. 20231

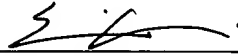
Sir:

In accordance with the provisions of 37 C.F.R. 1.56 and 37 C.F.R. 1.97-1.99, it is requested that the reference(s) listed on the attached Form PTO-1449 be made of record in the above-identified application.

The M. Sekiya article listed on the enclosed Form PTO-1449 was previously submitted in an Information Disclosure Statement on May 15, 2000 with a concise statement. It is being resubmitted at this time so that the Examiner may review and consider the English translation.

The Commissioner is hereby authorized to charge fees under 37 C.F.R. §§1.16, 1.17, 1.20(a), 1.20(b), 1.20(c), and 1.20(d) (except the Issue Fee) which may be required now or hereafter, or credit any overpayment to Deposit Account No. 19-2380. A duplicate copy of this sheet is attached.

Respectfully submitted,



Eric J. Robinson
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